

/ Descriptions

The BRCM24C02SC is 2Kbit I²C-compatible Serial EEPROM (Electrically Erasable Programmable Read-Only Memory) device in a SOP-8 Plastic Package. Halogen-free Product.

/ Features

Single Supply Voltage*

Minimum operating voltage down to 1.7V* 1 MHz clock from 2.5V to 5.5V* 400kHz clock from 1.7V to 2.5V*

Low power CMOS technology* Read current 400uA, maximum* Write current 1.6mA, maximum* Schmitt Trigger, Filtered Inputs for Noise Suppression*

Sequential & Random Read Features*

8-byte Page Write Modes*

Write protect of the whole memory array*

Additional Write Lockable Page and 128-bit Serial Number*

Self-timed Write Cycle (5ms maximum)*

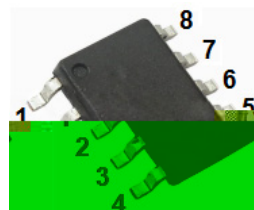
High Reliability* Endurance: > 1 Million Write Cycles* Data Retention: > 100 Years* HBM: 6KV*

Latch up Capability: +/-200mA;

/ Applications

Household appliances, Network communications, Portable Bluetooth devices, Set-top boxes, Smart meters, Fingerprint unlocking devices

/ Pinning



/ Pinning

/ Marking

/ See Marking Instructions

/ Absolute Maximum Ratings(Ta=25)

Parameter	Symbol	Rating	Unit
Storage Temperature	T _{stg}	-65~+150	
Operation Temperature	T _{opr}	-40~+85	
Maximum Operation Voltage	V _{cc}	6.25	V
Voltage on Any Pin with Respect to Ground	V _{pin}	-1.0~ V _{cc} +1.0	V
DC Output Current	I _{out}	5.0	mA
Electro-Static discharge ^① HBM mode ^②	ESD	6000	V

/ Reliability Characteristic

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Endurance	EDR	25℃~ 3.3V Page mode	1,000,000			Write cycles
Data retention	DRET		100			Years

Parameter	Symbol
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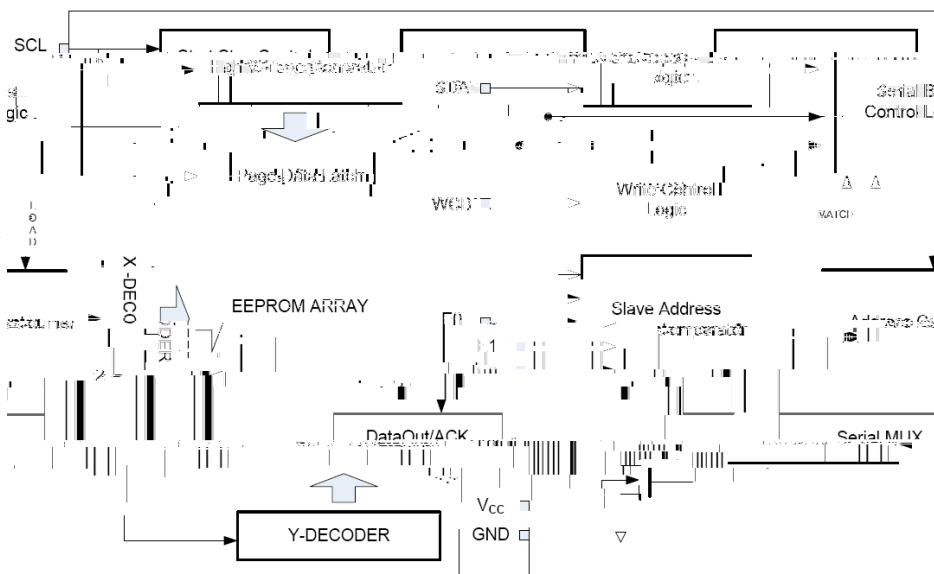
/ AC Electrical Characteristics(Unless otherwise specified, Vcc = 1.7V to 5.5V,
T_A = -40°C to 85°)

Parameter	Symbol	1.7V Vcc<2.5V			2.5V Vcc 5.5V			Unit
		Min	Typ	Max	Min	Typ	Max	
Data In Hold Time	t _{HD.DAT}	0	-	-	0	-	-	us
Data In Setup Time	t _{SU.DAT}	0.1	-	-	0.1	-	-	us
Inputs Rise Time[1]	t _R	-	-	0.3	-	-	0.3	us
Inputs Fall Time[1]	t _F	-	-	0.3	-	-	0.1	us
Stop Setup Time	t _{SU.STO}	0.6	-	-	0.25	-	-	us
Data Out Hold Time	t _{DH}	0.05	-	-	0.05	-	-	us
WCB pin Setup Time	t _{SU.WCB}	1.2	-	-	0.6	-	-	us
WCB pin Hold Time	t _{HD.WCB}	1.2	-	-	0.6	-	-	us
Write Cycle Time	t _{WR}	-	-	5	-	-	5	ms

Notes:AC measurement conditions:

1. R_L (connects to Vcc): 1.3k (2.5V, 5.5V), 10k (1.7V)
2. Input pulse voltages: 0.3 Vcc to 0.7 Vcc
3. Input rise and fall times: 50ns
4. Input and output timing reference voltages: 0.5Vcc

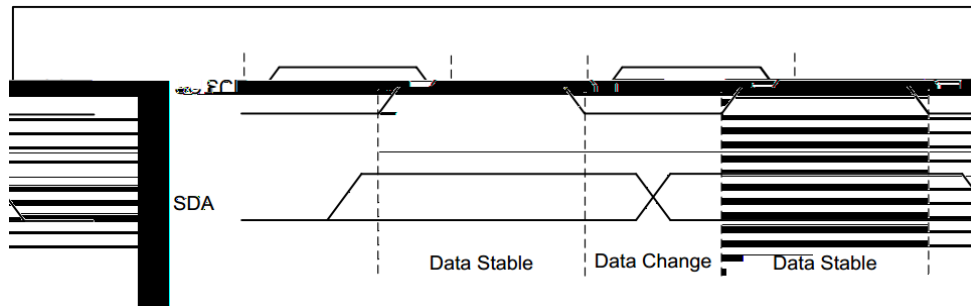
/ Functional Block Diagram



/ Functional Description

p / Data Input

The SDA pin is normally pulled high with an external device. Data on the SDA pin may change only during SCL low time periods (see to Figure 1). Data changes during SCL high periods will indicate a start or stop condition as defined below.

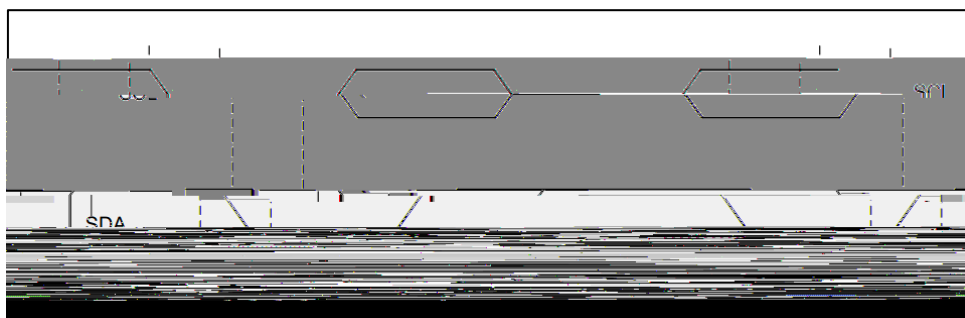


p / Start Condition

A high-to-low transition of SDA with SCL high is a start condition which must precede any other command (see to Figure 2).

p / Stop Condition

A low-to-high transition of SDA with SCL high is a stop condition. After a read sequence, the stop command will place the BRCM24C02SC in a standby power mode (see Figure 2).



p ACK / Acknowledge

/ Functional Description

All addresses and data words are serially transmitted to the BRCM24C02SC in 47-bit words. The data word is 16-bit.

/ Functional Description

Chip	Access area	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
BRCM24C02SC	Normal Area	1	0	1	0	E2	E1	E0	R/W
	ID Page	1	0	1	1	E2	E1	E0	R/W
	Lock Bit	1	0	1	1	E2	E1	E0	R/W
	Serial Number	1	0	1	1	E2	E1	E0	1

Table 1 Device Address

Chip	Access area	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
BRCM24C02SC	Normal Area	A7	A6	A5	A4	A3	A2	A1	A0
	ID Page	0	0	A5	A4	A3	A2	A1	A0

/ Functional Description

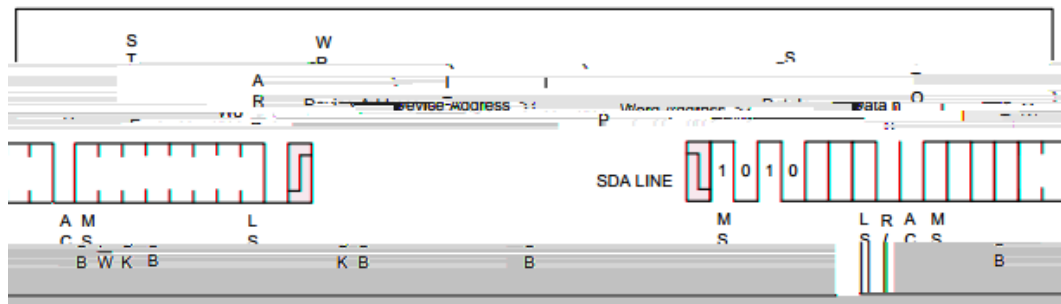


Figure 5 Byte Write

p / Page Write

A page write is initiated the same as a byte write, but the master does not send a stop condition after the first data word is clocked in. Instead, after the BRCM24C02SC acknowledges receipt of the first data word, the master can transmit more data words. The BRCM24C02SC will respond with a “0” after each data word received. The microcontroller must terminate the page write sequence with a stop condition.

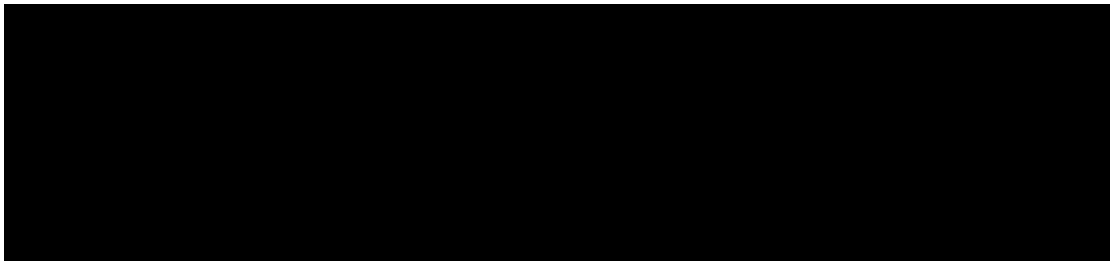


Figure 6 Page Write

The lower three bits of the data word address are internally incremented following the receipt of each data word. The higher data word address bits are not incremented, retaining the memory page row location. When the word address, internally generated, reaches the page boundary, the following byte is placed at the beginning of the same page. If more than 8 data words are transmitted to the BRCM24C02SC, the data word address will roll-over, and previous data will be overwritten. The address roll-over during write is from the last byte of the current page to the first byte of the same page.

/ Functional Description

p / Acknowledge Polling

Once the internally timed write cycle has started and the BRCM24C02SC inputs are disabled, acknowledge polling can be initiated. This involves sending a start condition followed by the device address word. The read/write bit is representative of the operation desired. Only if the internal write cycle has completed will the BRCM24C02SC respond with a “0”, allowing the read or write sequence to continue.

p / Write Identification Page

The Identification Page (16 bytes) is an additional page which can be written and (later) permanently locked in Read-only mode. It is written by the Write Identification Page instruction. This instruction uses the same protocol and format as Page Write (into memory array), except for the following differences:

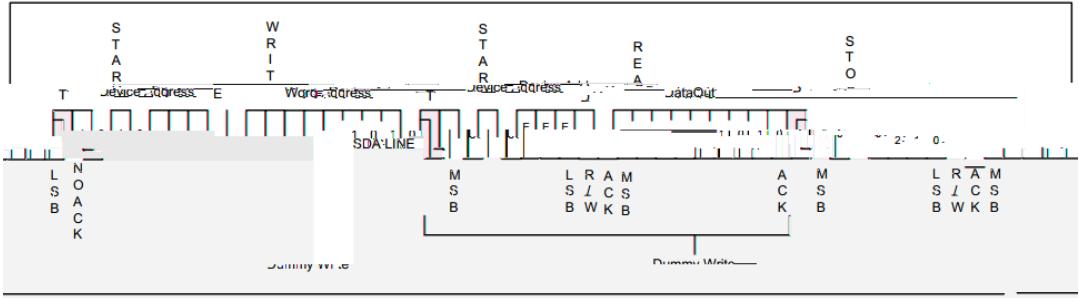
- (a) Device type identifier = 1011b.
- (b) Address bits A5/A4 are don't care while address bit A7/A6 which must be '00'.
- (c) Address bits A3/A0 define the byte address inside the Identification page. If the Identification page is locked, the data bytes transferred during the Write Identification Page instruction are not acknowledged (NoACK).

p / Lock Identification Page

The Lock Identification Page instruction (Lock ID) permanently locks the Identification page in Read-only mode. The Lock ID instruction is similar to Byte Write (into memory array) with the following specific conditions:

- (a) Device type identifier = 1011b.
- (b) Address bit A6 must be '1' all other address bits are don't care.
- (c) The data byte must be equal to the binary value xxxx xx1x, where x is don't care.

/ Functional Description



p

p / Read Identification Page

p / Read the Lock Status

p / Read Serial Number

/ Functional Description

The Identification Page (16 bytes) is an additional page which can be written and (later) permanently locked in Read-only mode.

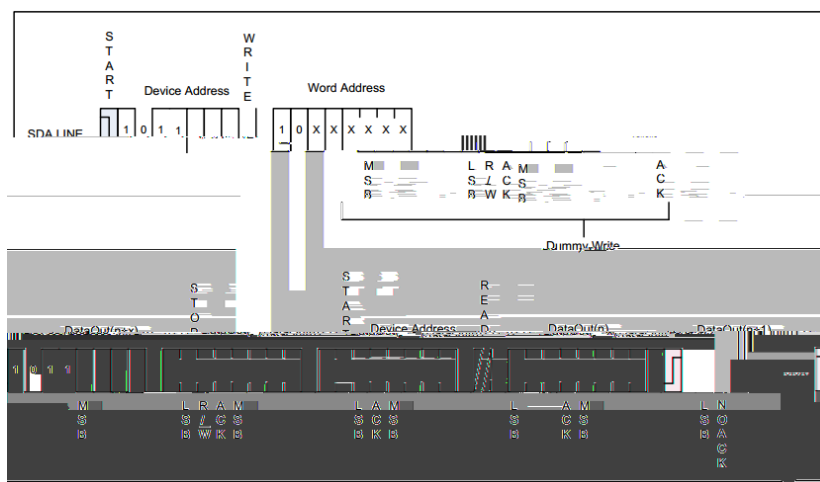
Reading the serial number is similar to the sequential read sequence but requires use of the device address seen in Table 1 on page 9, a dummy write, and the use of a specific word address. The entire 128-bit value must be read from the starting address of the serial number block to guarantee a unique number.

Since the address pointer of the device is shared between the regular EEPROM array and the serial number block, a dummy write sequence, as part of a Random Read or Sequential Read protocol, should be performed to ensure the address pointer is set to zero. A Current Address Read of the serial number block is supported but if the previous operation was to the EEPROM array, the address pointer will retain the last location accessed, incremented by one. Reading the serial number from a location other than the first address of the block will not result in a unique serial number.

Additionally, the word address contains a '10' sequence in bit A7 and A6 of the word address, regardless of the intended address as depicted in Table 2 . If a word address other than '10' is used, then the device will output undefined data.

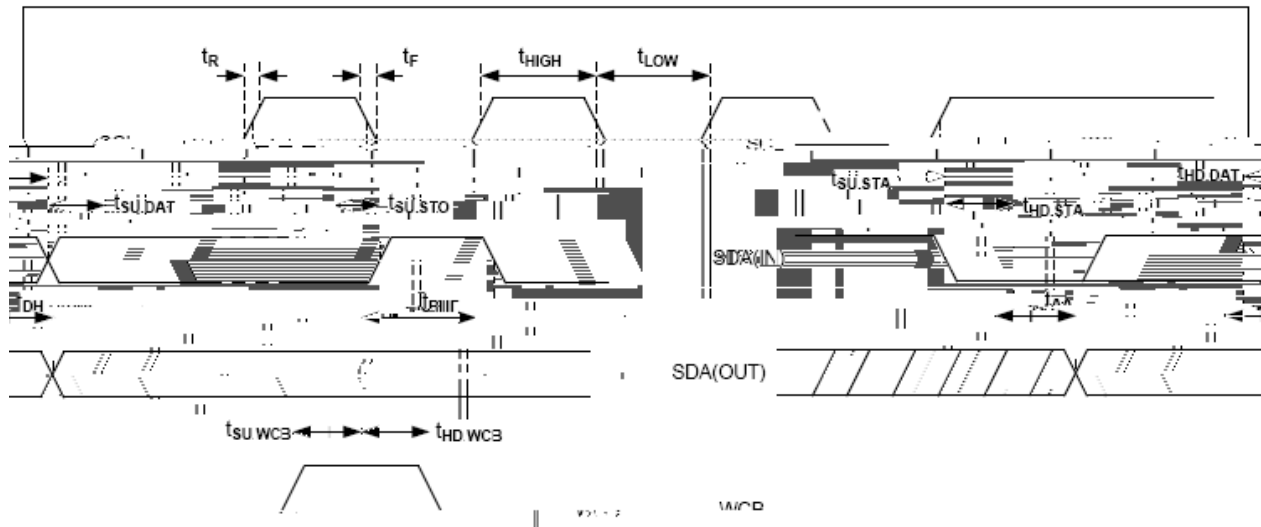
Example: If the application desires to read the first byte of the serial number, the word address input would need to be 80h.

When the end of the 128-bit serial number is reached (16 bytes of data), continued reading of the extended memory region will result in repeated serial number data readout for the data word address will roll-over back to the beginning of the 128-bit serial number. The Serial Number Read operation is terminated when the microcontroller does not respond with a zero (ACK) and instead issues a Stop condition (see Figure 11)



/ Time Sequence Diagram

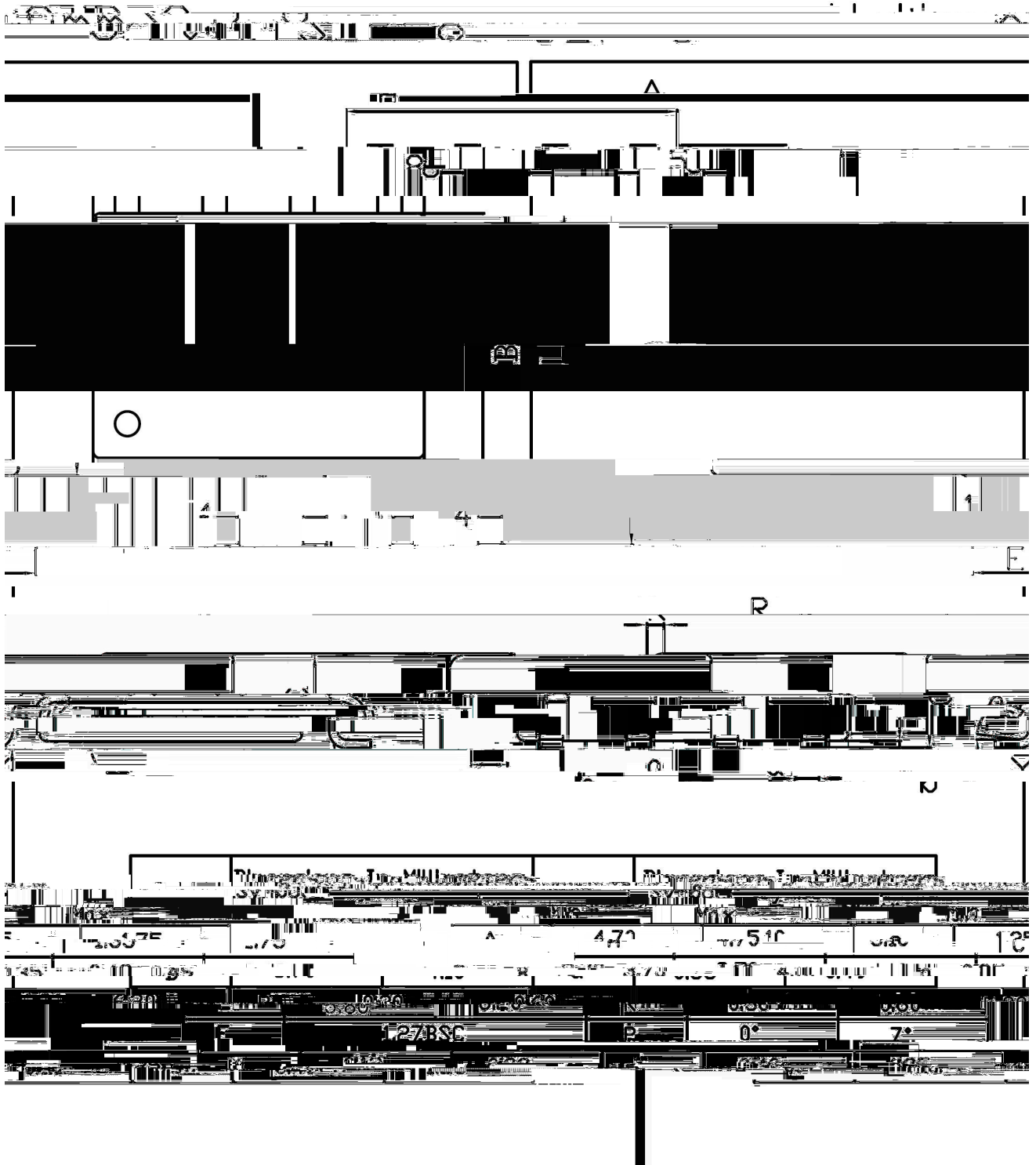
Bus Timing



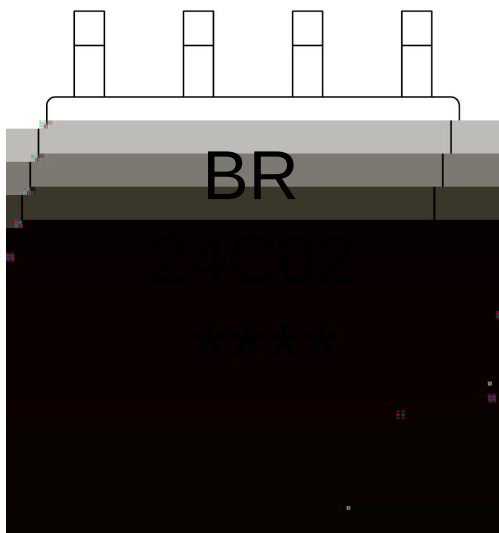
Write Cycle Timing



/ Package Dimensions



/ Marking Instructions



BR

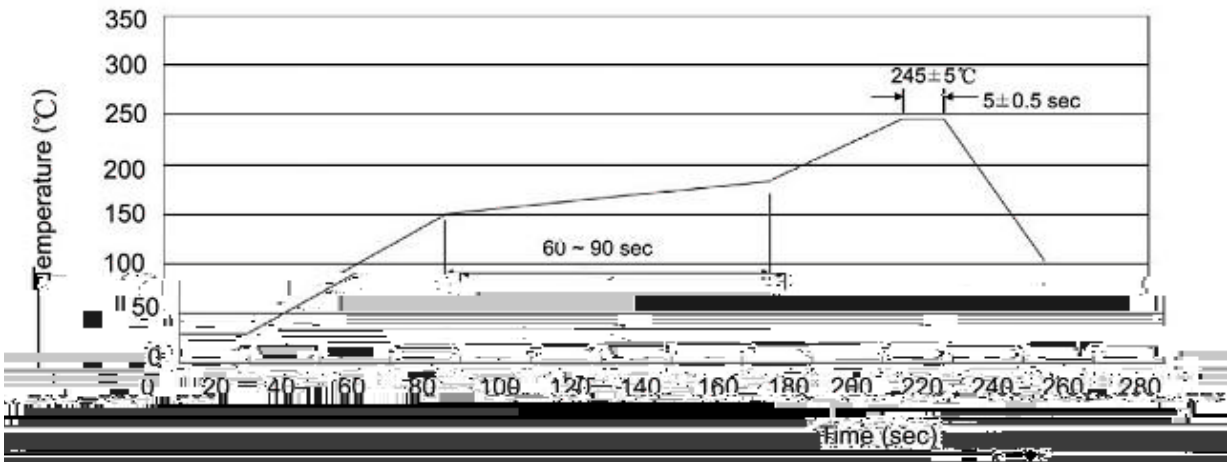
Note:

BR: Company Code.

24C02: Product Type.

****: Lot No. Code, code change with Lot No.

() / Temperature Profile for IR Reflow Soldering(Pb-Free)



Note:

- 1

150 180

60 90sec;
- 2

245 5

5 0.5sec;
- 3

2 10

/sec.
- 1.Preheating:150~180

, Time:60~90sec.
- 2.Peak Temp.:245 5

, Duration:5 0.5sec.
3. Cooling Speed: 2~10

/sec.

Y / Resistance to Soldering Heat Test Conditions

260 5

10 1 sec

Temp.:260 5

Time:10 1 sec

/ REEL

Package Type	Units					Dimension (unit mm ³)		
	Units/Reel	Reels/Inner Box	Units/Inner Box	Inner Boxes/Outer Box	Units/Outer Box	Reel	Inner Box	Outer Box
SOP/ESOP-8	4,000	2	8,000	6	48,000	13 p ×12	360×360×50	380×335×366×12